

EAST - [thinsearch15.wsp:1]

File View Edit Tools Window Help

☐ L1: (2) "20040042298"
☐ L2: (2) "6545903"
☐ L3: (42946) phase adj change\$
☐ L4: (9489) 3 and memory
☐ L5: (62373) barrier adj layer
☐ L6: (1198) metal adj barrier adj layer\$1
☐ L7: (4) 6 and 4
☐ L8: (158) ovonic and memory
☐ L9: (34) 5 and 8
☐ L10: (687) GTS
☐ L11: (1) 4 and 5 and 10
☐ L12: (9) 4 and GTS
☐ L13: (0) 8 and 6
☐ L14: (4) 4 and 6
☐ L15: (234) 4 and 5
☐ L16: (50) nitride
☐ L17: (28440) Titanium adj nitride
☐ L18: (77) 15 and 17
☐ L19: (17526) common adj source
☐ L20: (5) common adj source adj plug
☐ L21: (289) 19 and memory and transistor and plug

Failed

Search:
 DB:
 Default operator: OR
☐ Boolean ☐ Highlight all hits in index
 19 and memory and transistor and plug

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	2	3	Image 1
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20040090195 AI	20040513	61	Efficient control, monitoring and energy devices for vehicles such as w	318/109			Motsenbocker, Marvin A.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 2004
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040087084 AI	20040506	16	Self-aligned structure with unique erasing gate in split gate flash	438/257			Hsieh, Chia-Ta	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 2004
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040084702 AI	20040506	32	Magnetic memories with bit lines and digit lines that intersect at oblique and	257/295			Jeong, Won-Cheol	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 2004
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040079970 AI	20040429	16	NAND flash memory	257/210			Hosono, Koji et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 2004
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20040077144 AI	20040422	16	Method to form self-aligned split gate flash with L-shaped wordline spacers	438/257			Hsieh, Chia-Ta	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 2004
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040076050 AI	20040422	19	Flash memory cell with high programming efficiency by coupling f	365/200			Hsieh, Chia-Ta	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 2004
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040075141 AI	20040422	28	Semiconductor device and method of manufacturing same	257/347			Maeda, Shigenobu et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 2004
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040071030 AI	20040415	69	Semiconductor integrated circuits, fabrication method for the same and s	365/222			Goda, Akira et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 2004
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040070007 AI	20040415	11	Vertical field effect transistors including conformal monocrystalline	257/200			Zhang, Zhibo	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 2004
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040058499 AI	20040325	65	Semiconductor device and manufacturing method of the same	438/296	257/506		Ishitsuka, Norio et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 2004
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20040056316 AI	20040325	15	Method and apparatus for a flash memory device comprising a source l	257/390	257/324; 257/391		Prall, Kirk D. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 2004

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